SUPPLY VOLTAGE MONITOR

ISSUE 3 – JULY 2006

DEVICE DESCRIPTION

The ZSM300 is a three terminal under voltage monitor circuit for use in microprocessor systems. The threshold voltage of the device has been set to 2.63 volts making it ideal for 3 volt circuits.

Included in the device is a precise voltage reference and a comparator with built in hysteresis to prevent erratic operation. The ZSM300 features an open collector output capable of sinking at least I0mA which only requires a single external resistor to interface to following circuits.

Operation of the device is guaranteed from one volt upwards, from this level to the device threshold voltage the output is held low providing a power on reset function. Should the supply voltage, once established, at any time drop below the threshold level then the output again will pull low.

The device is available in a TO92 package for through hole applications as well as SOT223 for surface mount requirements.

FEATURES

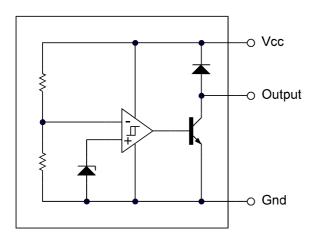
- SOT223 and TO92 packages
- Power on reset generator
- Automatic reset generation
- Low standby current
- Guaranteed operation from 1 volt
- Wide supply voltage range
- Internal clamp diode to discharge delay capacitor
- 2.63 volt threshold for 3 volt logic
- 20mV hysteresis prevents erratic operation

ZSM300

APPLICATIONS

- Microprocessor systems
- Computers
- Computer peripherals
- Instrumentation
- Automotive
- Battery powered equipment

SCHEMATIC DIAGRAM





ZSM300

ABSOLUTE MAXIMUM RATING

Input Supply Voltage	-1 to 10V
Offstate Output Voltage	10V
Onstate Output Sink Current(Note 1)	Internally limited
Clamp Diode Forward Current(Note 1)	100mA
Operating Junction Temperature	150°C
Operating Temperature	-40 to 85°C
Storage Temperature	-55 to 150°C

Power Dissipation

TO92 SOT223 780mW 2W(Note 2)

TEST CONDITIONS (Tamb=25°C for typical values, Tamb=-40 to 85°C for min/max values (Note3)) **COMPARATOR**

PARAMETER	SYMBOL	MIN	TYP.	MAX.	UNITS
Threshold Voltage High state output (V _{cc} increasing)	V _{IH}	2.56	2.64	2.7	V
Threshold Voltage Low state output (V _{cc} decreasing)	V _{IL}	2.56	2.62	2.7	V
Hysteresis	V _H	0.01	0.02	0.05	V

OUPUT

Output sink saturation:	V _{OL}				
(V _{cc} =2.2V, I _{sink} =8.0mA)			0.46	1.0	V
(V _{cc} =2.2V, I _{sink} =2.0mA)			0.15	0.4	V
(V _{cc} =1.0V, I _{sink} =0.1mA)				0.25	V
Onstate output sink current (V _{cc} , Output=2.2V)	I _{sink}	10	27	60	mA
Offstate output leakage current (V _{cc} , Output=3V)	I _{oh}		0.02	0.5	μA
Clamp diode forward voltage (I _f =10mA)	V _f	0.6	1.2	1.5	V
Propagation delay (V _{in} 3V to 2.2V, R _I =10k, T _{amb} =25°C)	T _d		2.5		μs

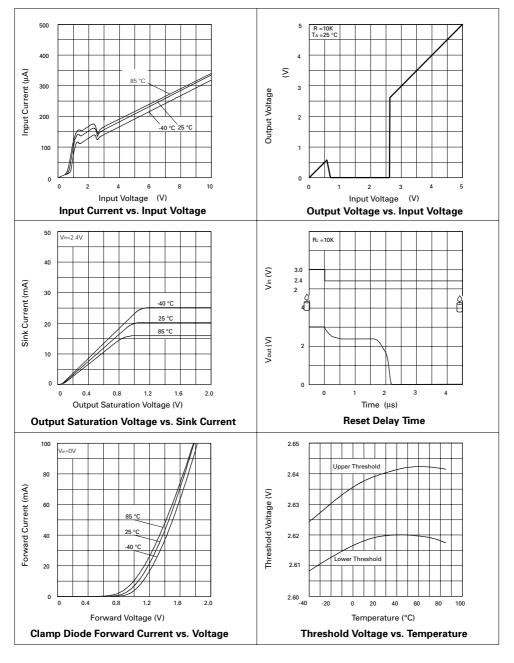
TOTAL DEVICE

Operating input voltage range	V _{cc}	1.0 to 6.5			V
Quiescent input current (V_{cc} =3V)	lq		125	190	μA

Note:

Maximum package power dissipation must be observed.
Maximum power dissipation for the SOT223 package is calculated assuming that the device is mounted on a PCB measuring 2 inches square.
Low duty cycle pulse techniques are used during test to maintain junction temperatures as close to ambient as possible.

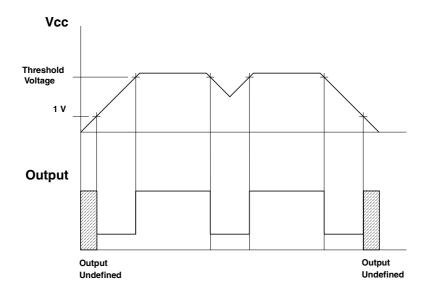
ZSM300



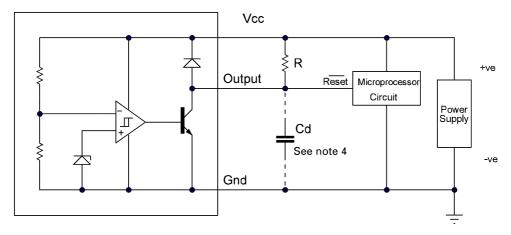
TYPICAL CHARACTERISTICS

ZSM300

TIMING DIAGRAM



APPLICATION CIRCUIT

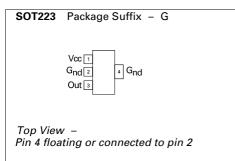


Note 4: A time delayed reset can be accomplished with the additional Cd.

$$T_{DY} = RCd \ln \left(\frac{1}{1 - \frac{V_{TH(mpu)}}{V_{in}}} \right)$$

 $\begin{array}{ll} T_{DY} & = Time \; (Seconds) \\ V_{TH} & = Microprocessor \; Reset \; Threshold \\ V_{in} & = Power \; Supply \; Voltage \end{array}$

CONNECTION DIAGRAMS



TO92 Package Su	ffix – C
G _{nd} Vcc Out	• 1 • 2 • 3
Bottom View	

ORDERING INFORMATION

Part Number	Package	Part Mark
ZSM300G	SOT223	ZSM300
ZSM300C	TO92	ZSM300

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